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KEVIN S. ROSS (SENDER'S PRINTED NAME)

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Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Enclosed for filing is a patent application under 37 CFR 1.53(b) of:

Inventor(s):

Won-Sang Song, Jung-Woo Kim, Chang-Sub Lee, Sam-Young Kim,

Young-Jin Wee and Ki-Chul Park

For:

APPARATUS FOR TESTING RELIABILITY OF

INTERCONNECTION IN INTEGRATED CIRCUIT

This application is a continuation, divisional, continuation-in-part of prior application Serial No. 10/114,735, filed April 1, 2002.

Prior application info: Examiner: Minh Nhut Tang Group Art Unit: 2829

Applicant requests FIG. 9 to be published with the application.

## Enclosures:

Specification (pages 1-12); claims (pages 13-15); abstract (page 16)

EIGHT sheet(s) of FORMAL drawings (comprising 9 figures)

	CLAIM	IS AS FILED		
For	Number Filed	Number Extra	Rate	Basic Fee \$770
Total Claims	12	0	x \$ 18 =	\$0.00
Independent Claims	3	0	x \$ 86 =	\$0.00
TOTAL FILING FEE				\$770.00

$\boxtimes$	Combined Declaration and Power of Attorney (copy from prior application)					
	Copy from a prior application (37 CFR 1.63(d))					
	Incorporation by ReferenceThe entire disclosure of the prior application, from					
	which a copy of the oath or declaration is supplied is considered as being part of	f the				
	disclosure of the accompanying application and is hereby incorporated by refere therein.	nce				
$\boxtimes$	Recorded Assignment (copy from prior application)					
	Assignee Name and Address: Samsung Electronics Co., Ltd.					
	Suwon-city, Kyungki-do Republic of Koro	ea				
$\boxtimes$	PTO 1449 Information Disclosure Statement with Form with 4 cited references					
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	PTO Form 2038 authorizing credit card payment of \$770.00 (filing fee) is enclosed Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.					

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Respectfully submitted,

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